



**IEEE INTERNATIONAL SYMPOSIUM ON INDUSTRIAL ELECTRONICS ISIE'18
12-15 JUNE 2018, CAIRNS, AUSTRALIA**

Special Session on

“Power Electronics for HVDC Transmission and DC Networks”

Organized by

Georgios Konstantinou, UNSW Sydney, Australia, g.konstantinou@unsw.edu.au

Josep Pou, Nanyang Technological University, Singapore, j.pou@ntu.edu.sg

Zixin Li, Chinese Academy of Sciences, lzx@mail.iee.ac.cn

Call for Papers

More than 9GW in 12 Voltage-Source Converter High-Voltage Direct Current (VSC-HVDC) projects were completed in the last three years and 20 more are currently under commissioning and construction all over the world. The growth and research interest for HVDC technologies and dc networks, fuelled by developments in multilevel high-power power electronics, has been tremendous and will continue into the future. Power electronics topologies, their characteristics, operating principles and control strategies, fault tolerance, protection schemes and converter coordination of HVDC systems and the development of practical dc-breakers and dc-dc converter topologies for high-voltage applications require further investigation. This special session will focus on current and emerging power electronics technologies for HVDC transmission and dc networks.

Topics of interest include, but are not limited to:

- New multilevel VSC topology and MMC topology improvements
- VSC-LCC hybrid topologies for HVDC applications
- Models for simulation of multi-VSC and VSC-HVDC systems
- Control of multi-terminal VSC-based HVDC systems
- Control of VSC-LCC hybrid two-terminal and multi-terminal HVDC systems
- Fault characteristics and protection of VSC and VSC-LCC HVDC systems

All the instructions for paper submission are included in the conference website: <http://www.ieee-isie2018.org>